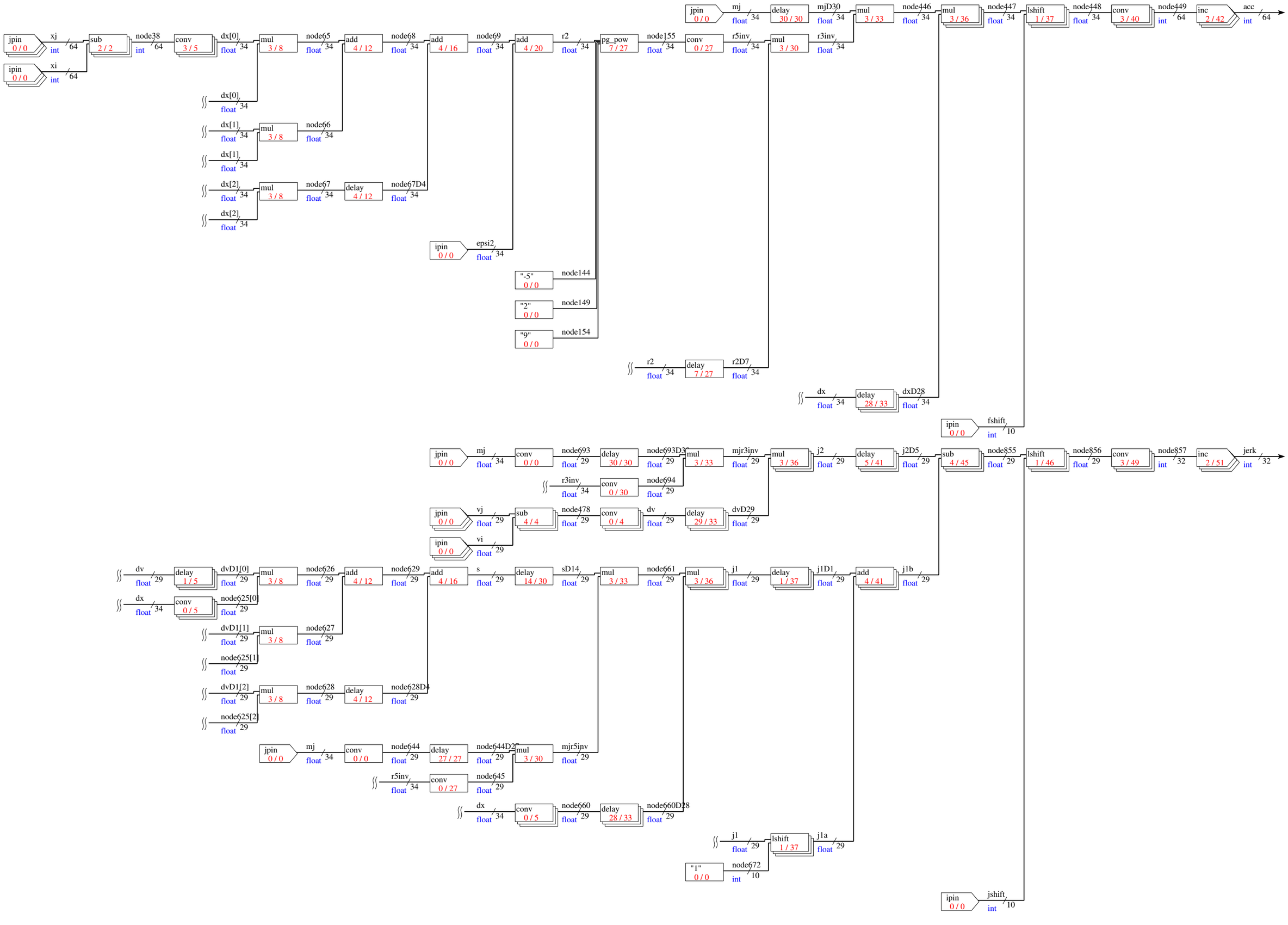
jpin 0 / 0
xj int 64
ipin 0 / 0
xi int 64
sub 2 / 2
node38 int 64
conv 3 / 5
dx[0] float 34
mul 3 / 8
node65 float 34
add 4 / 12
node68 float 34
add 4 / 16
node69 float 34
add 4 / 20
r2 float 34
pg_pow 7 / 27
node155 float 34
conv 0 / 27
r5inv float 34
mul 3 / 30
r3inv float 34
mul 3 / 33
node446 float 34
mul 3 / 36
node447 float 34
lshift 1 / 37
node448 float 34
conv 3 / 40
node449 int 64
inc 2 / 42
acc int 64
jpin 0 / 0
mj float 34
delay 30 / 30
mjD30 float 34
dx[0] float 34
dx[1] float 34
mul 3 / 8
node66 float 34
dx[1] float 34
dx[2] float 34
mul 3 / 8
node67 float 34
delay 4 / 12
node67D4 float 34
dx[2] float 34
ipin 0 / 0
epsi2 float 34
"-5" 0 / 0
node144
"2" 0 / 0
node149
"9" 0 / 0
node154
r2 float 34
delay 7 / 27
r2D7 float 34
dx float 34
delay 28 / 33
dxD28 float 34
ipin 0 / 0
fshift int 10
jpin 0 / 0
mj float 34
conv 0 / 0
node693 float 29
delay 30 / 30
node693D30 float 29
mul 3 / 33
mjr3inv float 29
mul 3 / 36
j2 float 29
delay 5 / 41
j2D5 float 29
sub 4 / 45
node855 float 29
lshift 1 / 46
node856 float 29
conv 3 / 49
node857 int 32
inc 2 / 51
jerk int 32
r3inv float 34
conv 0 / 30
node694 float 29
jpin 0 / 0
vj float 29
sub 4 / 4
node478 float 29
conv 0 / 4
dv float 29
delay 29 / 33
dvD29 float 29
ipin 0 / 0
vi float 29
dv float 29
delay 1 / 5
dvD1[0] float 29
mul 3 / 8
node626 float 29
add 4 / 12
node629 float 29
add 4 / 16
s float 29
delay 14 / 30
sD14 float 29
mul 3 / 33
node661 float 29
mul 3 / 36
j1 float 29
delay 1 / 37
j1D1 float 29
add 4 / 41
j1b float 29
dx float 34
conv 0 / 5
node625[0] float 29
dvD1[1] float 29
mul 3 / 8
node627 float 29
node625[1] float 29
dvD1[2] float 29
mul 3 / 8
node628 float 29
delay 4 / 12
node628D4 float 29
node625[2] float 29
jpin 0 / 0
mj float 34
conv 0 / 0
node644 float 29
delay 27 / 27
node644D27 float 29
mul 3 / 30
mjr5inv float 29
r5inv float 34
conv 0 / 27
node645 float 29
dx float 34
conv 0 / 5
node660 float 29
delay 28 / 33
node660D28 float 29
j1 float 29
lshift 1 / 37
j1a float 29
"1" 0 / 0
node672 int 10
ipin 0 / 0
jshift int 10